

Figure 1B

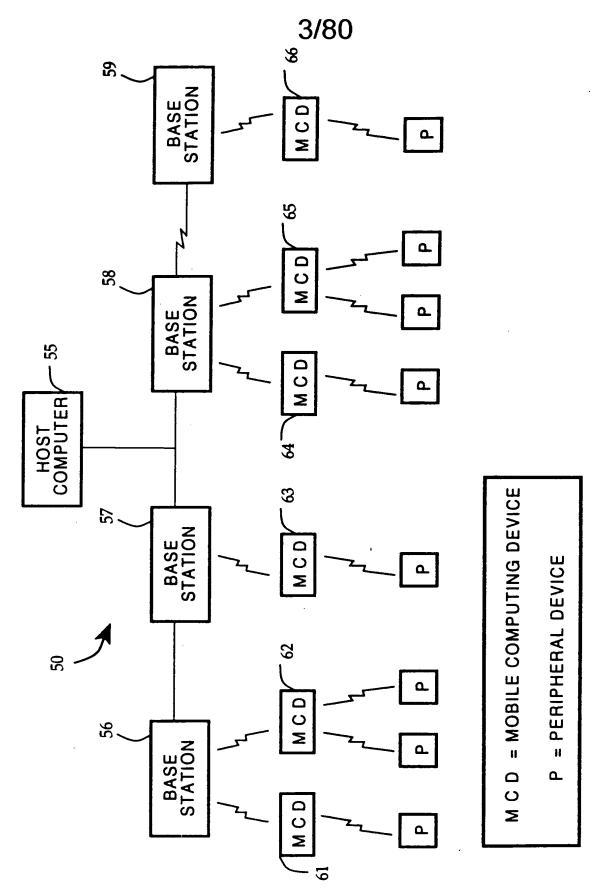


Figure 1C

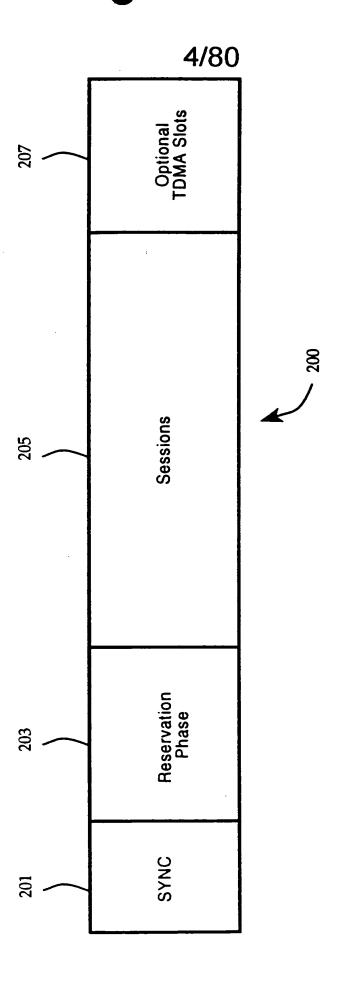


Figure 2

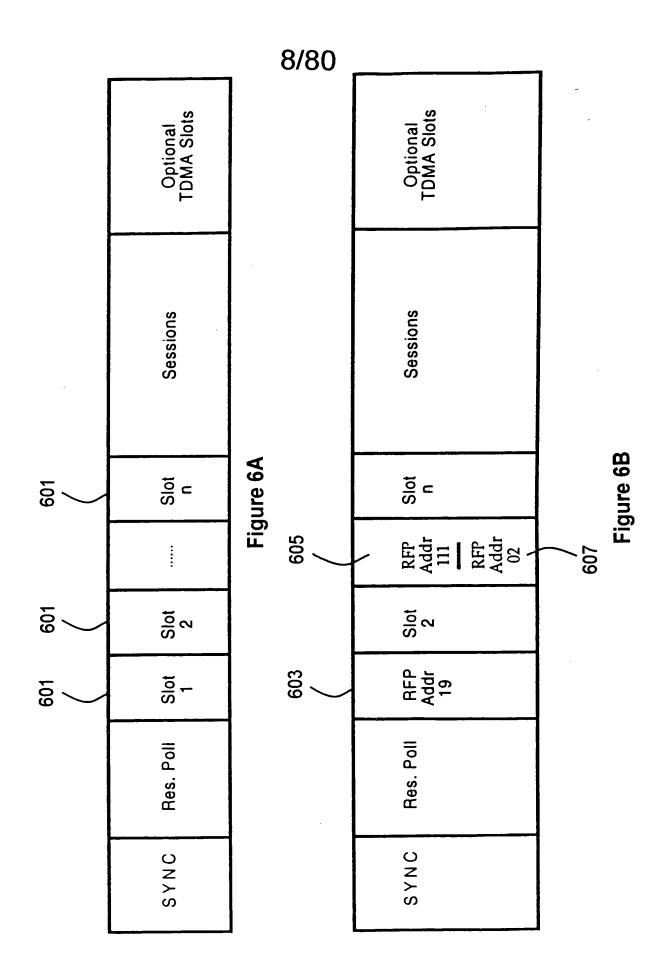
		5/80		T	7
10 ms	Freq. 1 2466 MHz	301	20 ms	Freq. 1 2466 MHz	303
10 ms	Freq. 79 2403 MHz	301	20 ms	Freq. 79 2403 MHz	303
10 ms			20 ms		
10 ms	Freq. 5 2444 MHz) 301 Figure 3A	20 ms	Freq. 5 2444 MHz	303
10 ms	Freq. 4 2472 MHz	301 Figu	20 ms	Freq. 4 2472 MHz	303
10 ms	Freq. 3 2417 MHz	301	20 ms	Freq. 3 2417 MHz	303
10 ms	Freq. 2 2411 MHz	301	20 ms	Freq. 2 2411 MHz	303
10 ms	Freq. 1 2466 MHz	301	20 ms	Freq. 1 2466 MHz	303

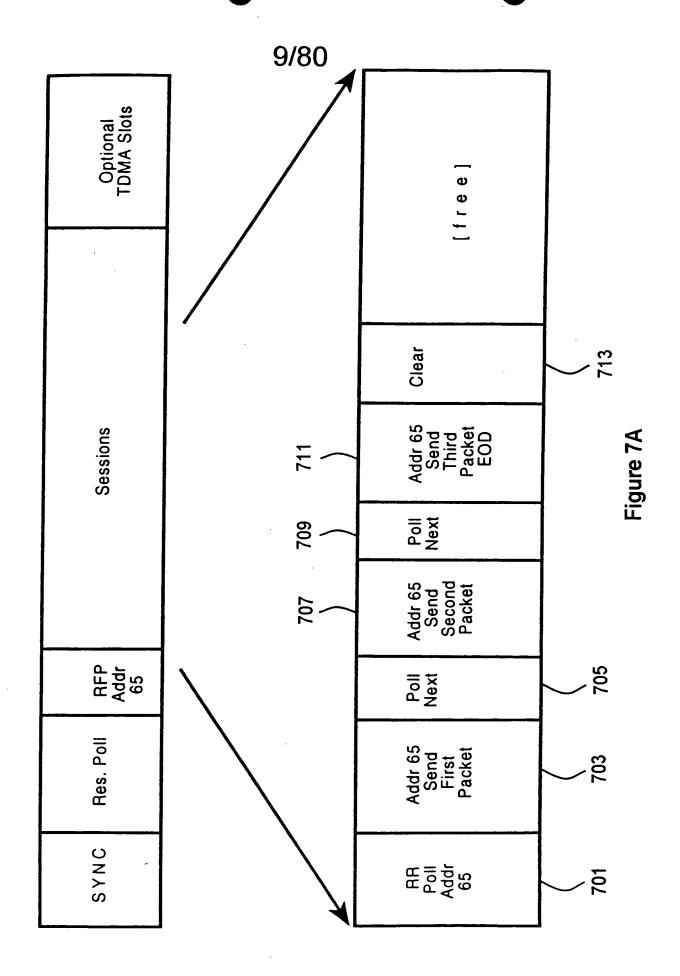
Figure 3B

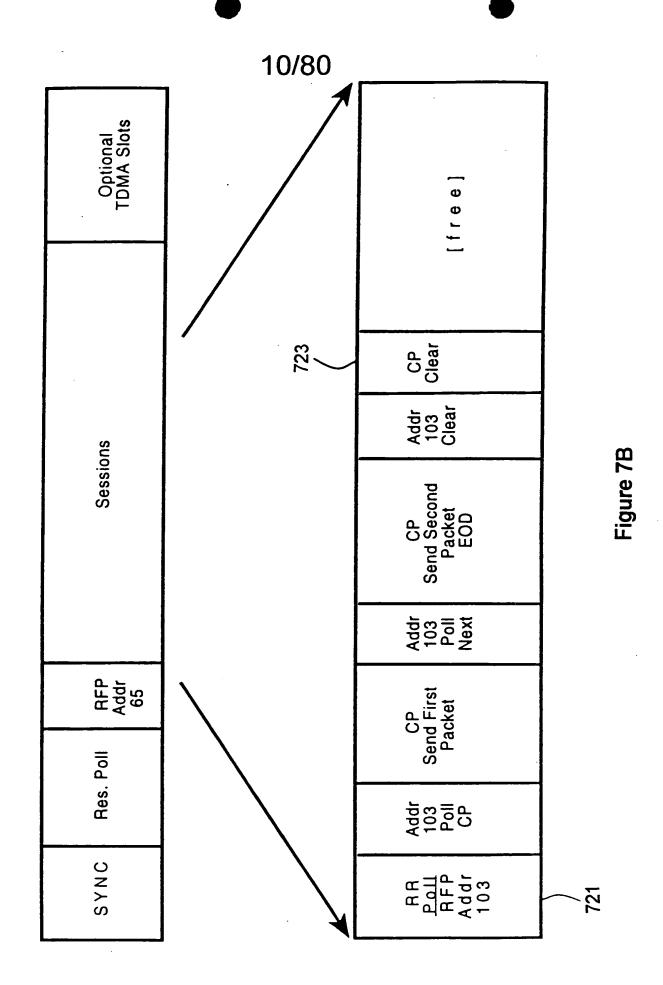
	Freq 1: 2466 MHz	6 MHz			Freq 2:	Freq 2: 2411 MHz	
AI. 1	AI. 2	AI. 3	AI. 4	AI. 1	AI. 2	AI. 3	AI. 4
20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms	20 ms

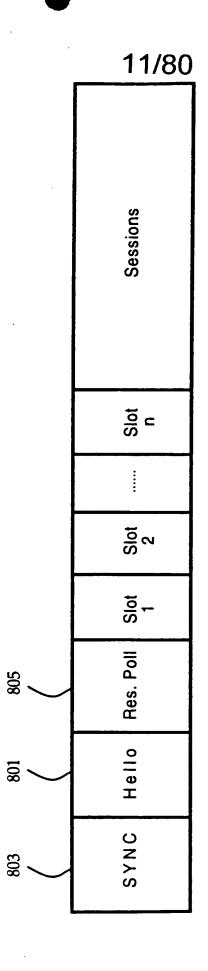
Figure 4B

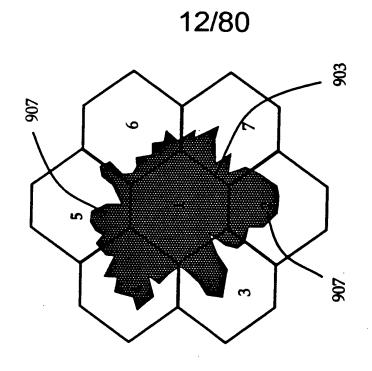
Figure 5B

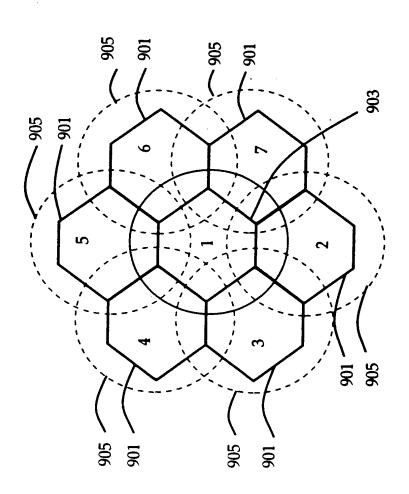


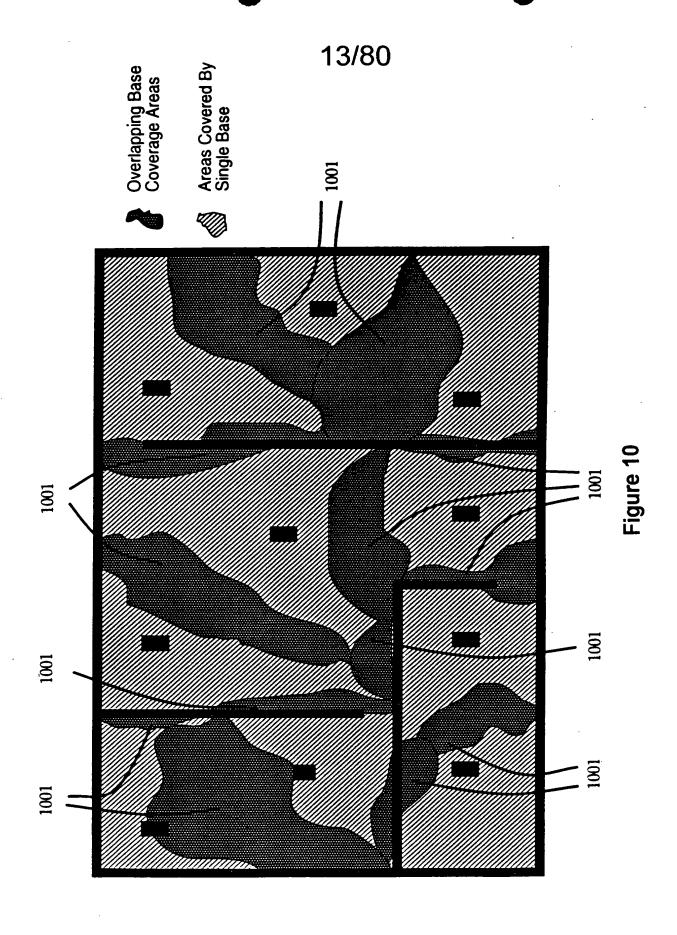












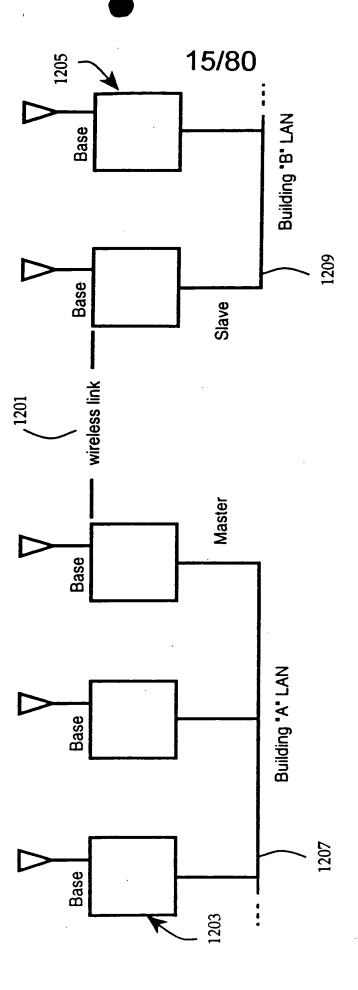


Figure 12

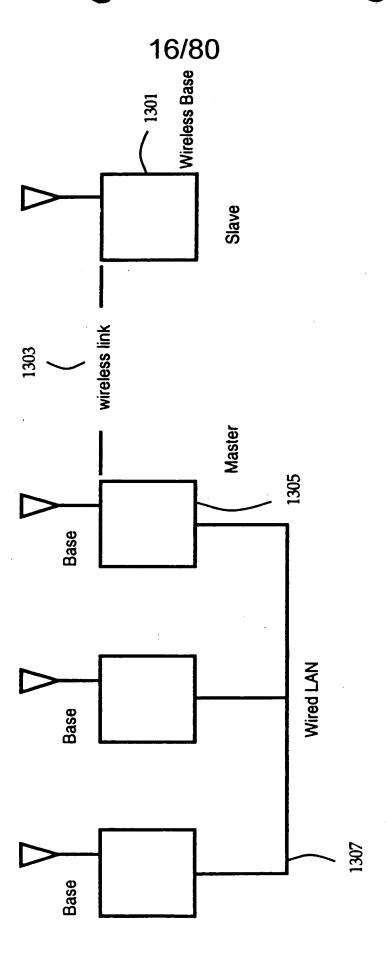
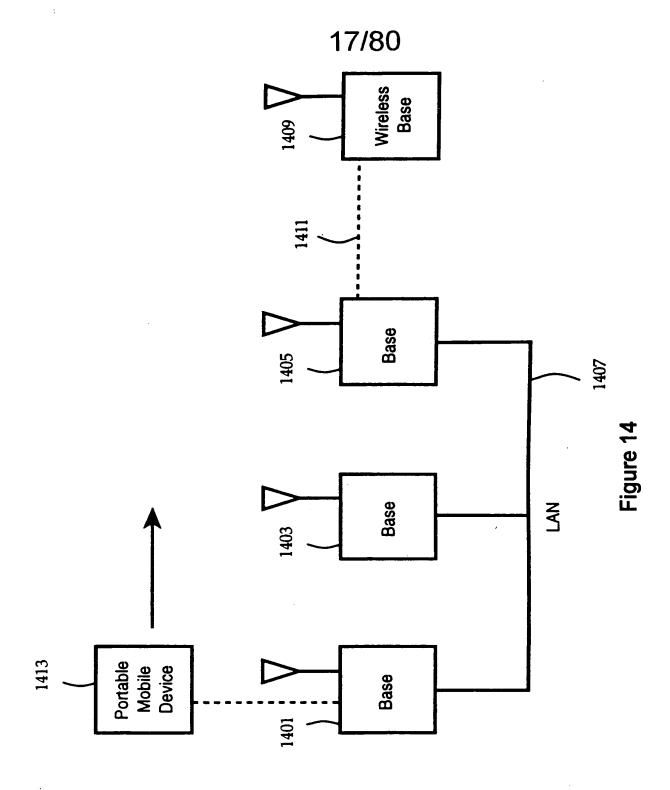


Figure 13



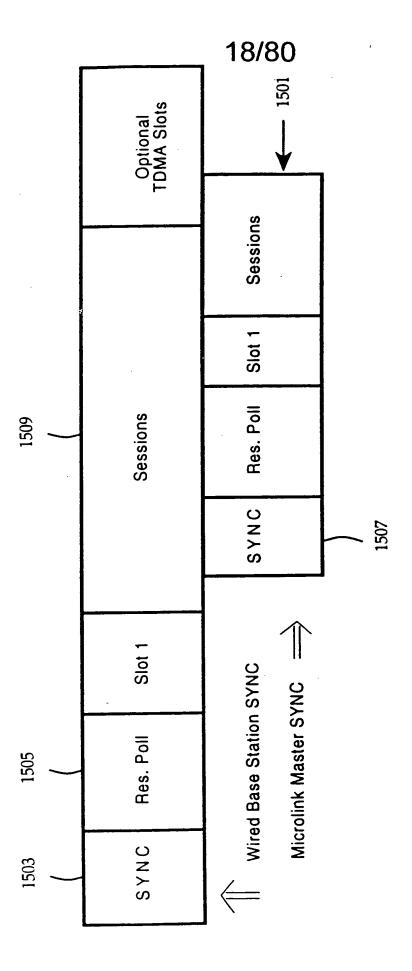


Figure 15

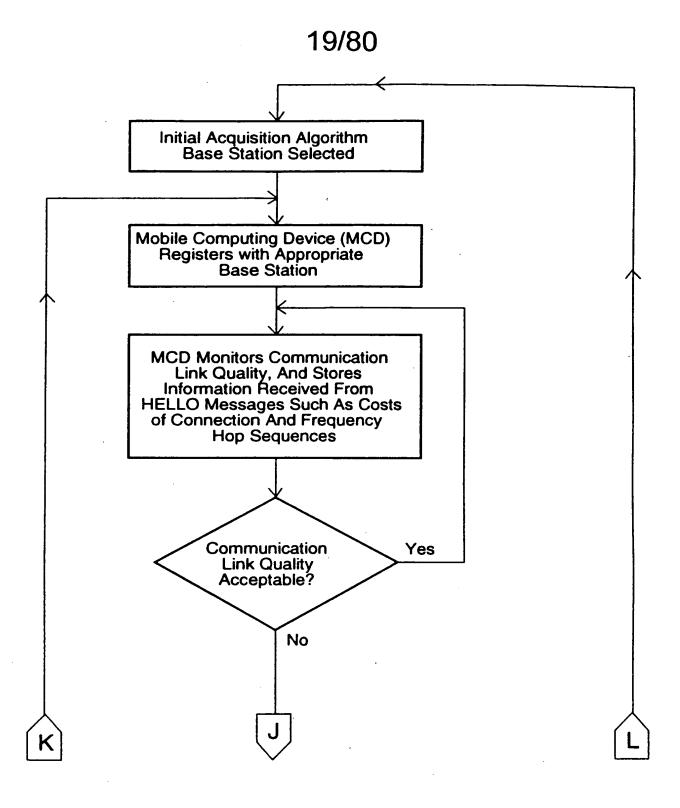
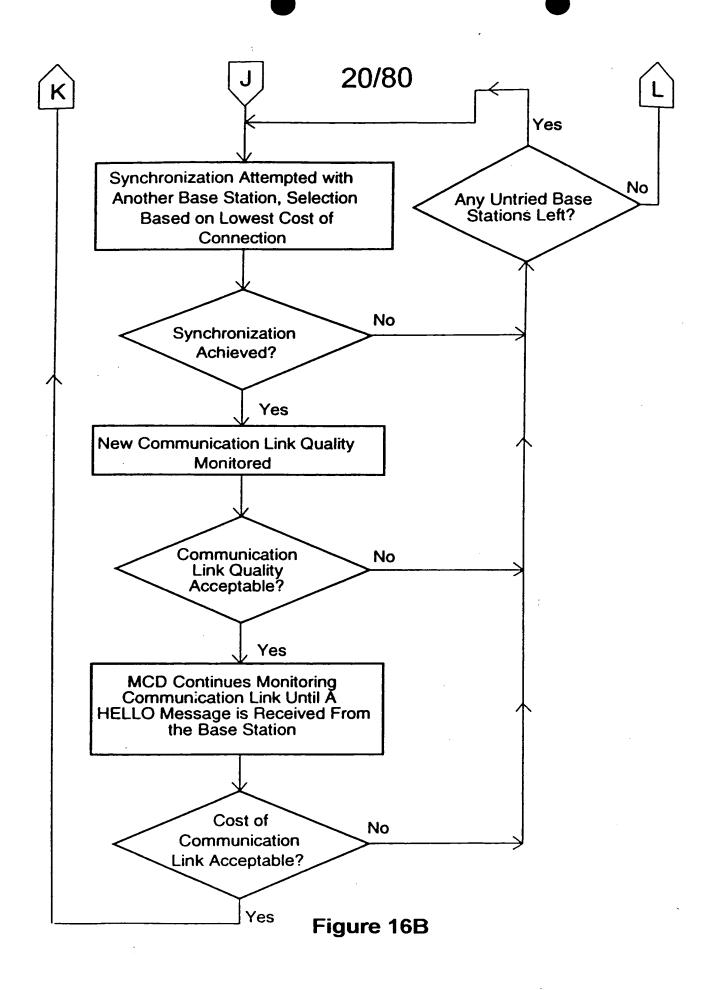


Figure 16A



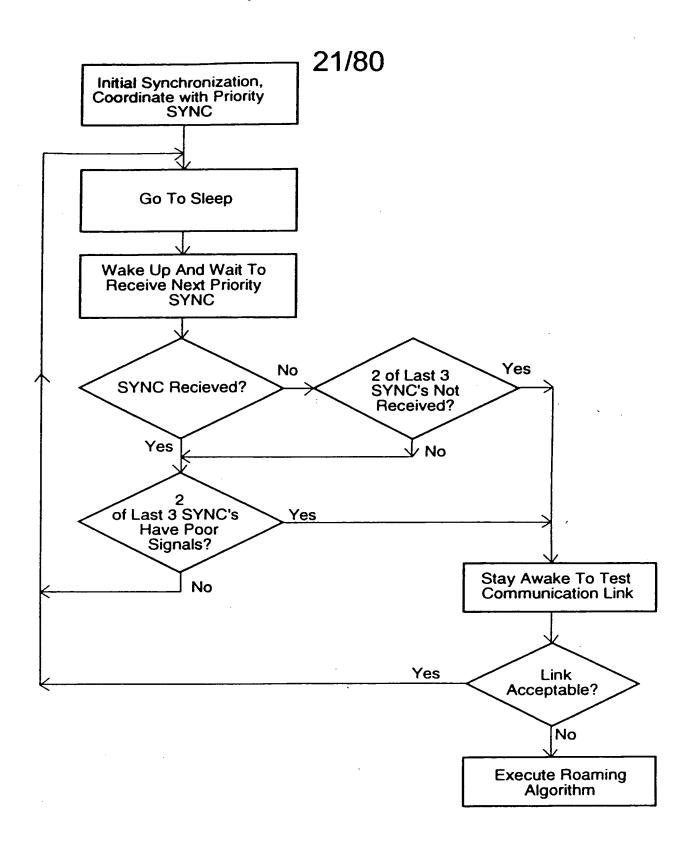
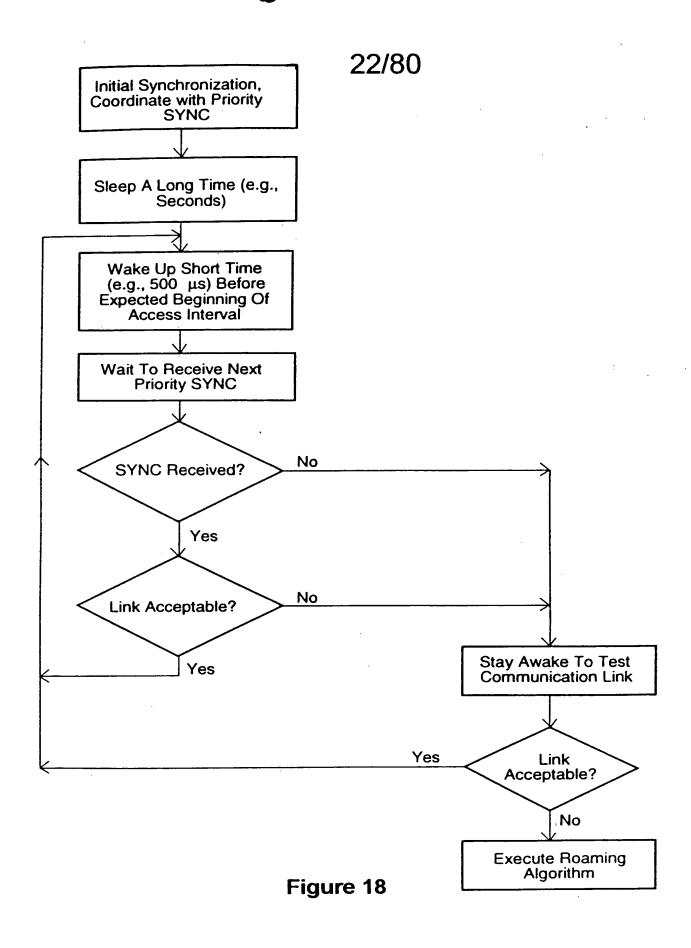
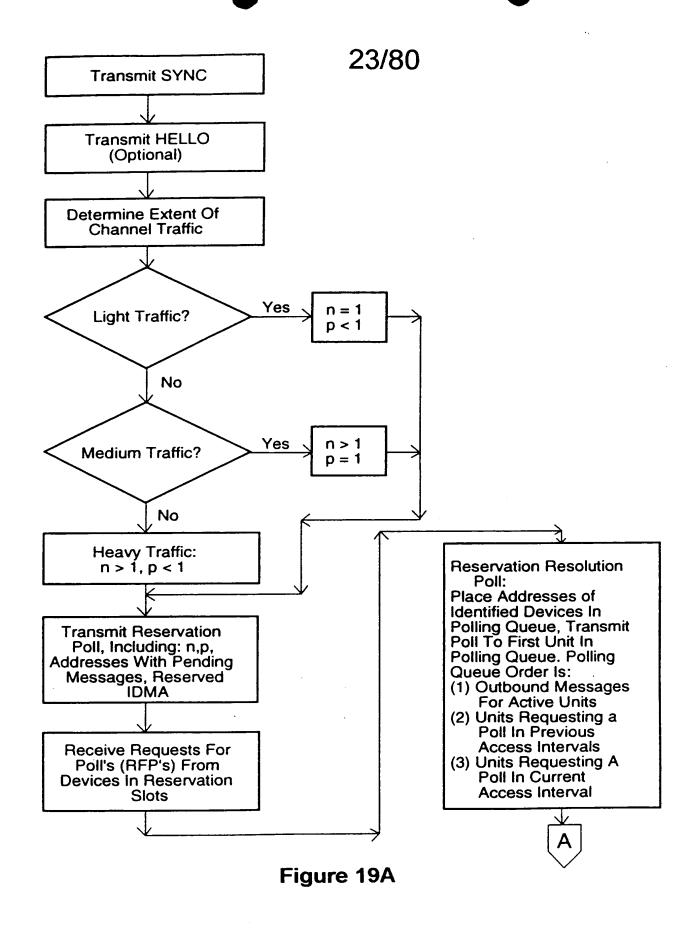


Figure 17





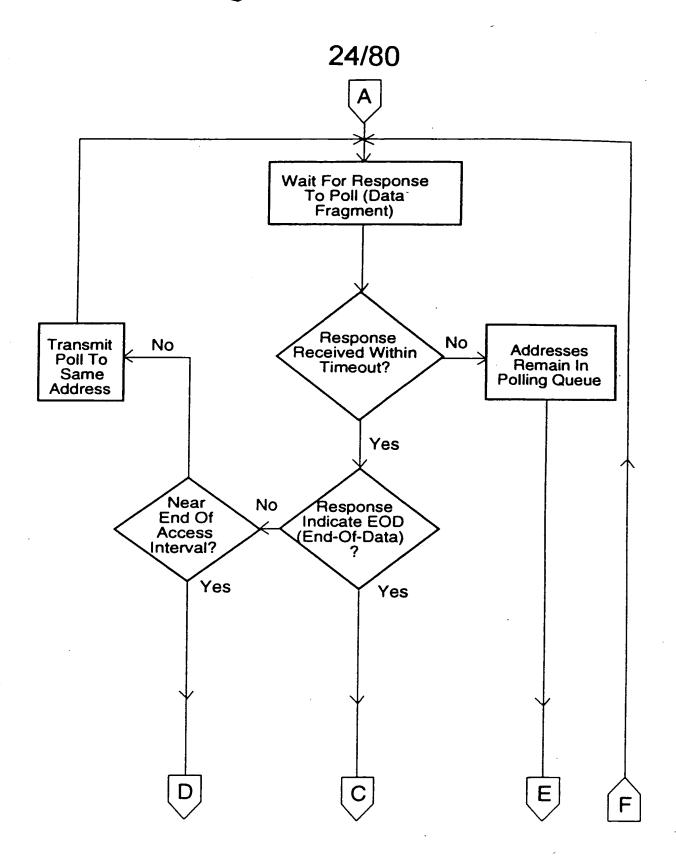


Figure 19B-1

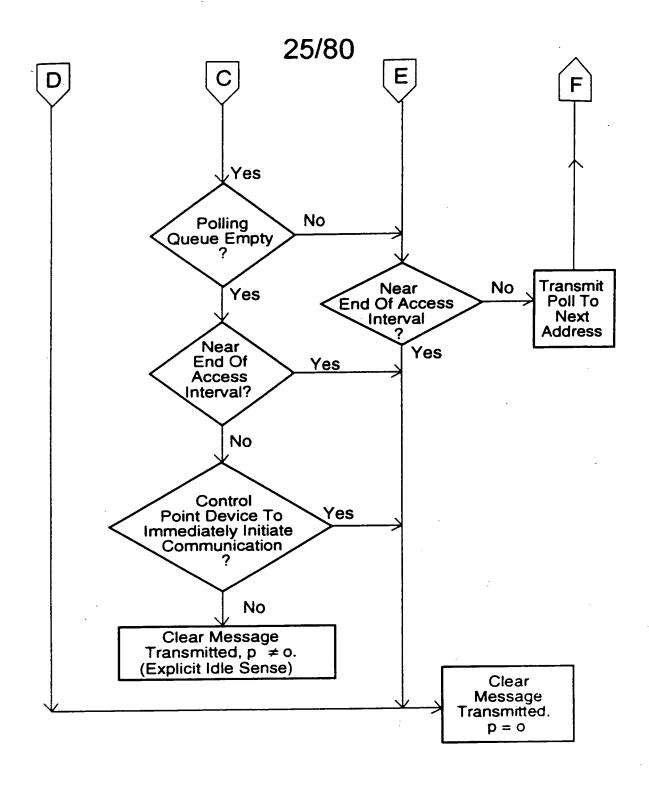
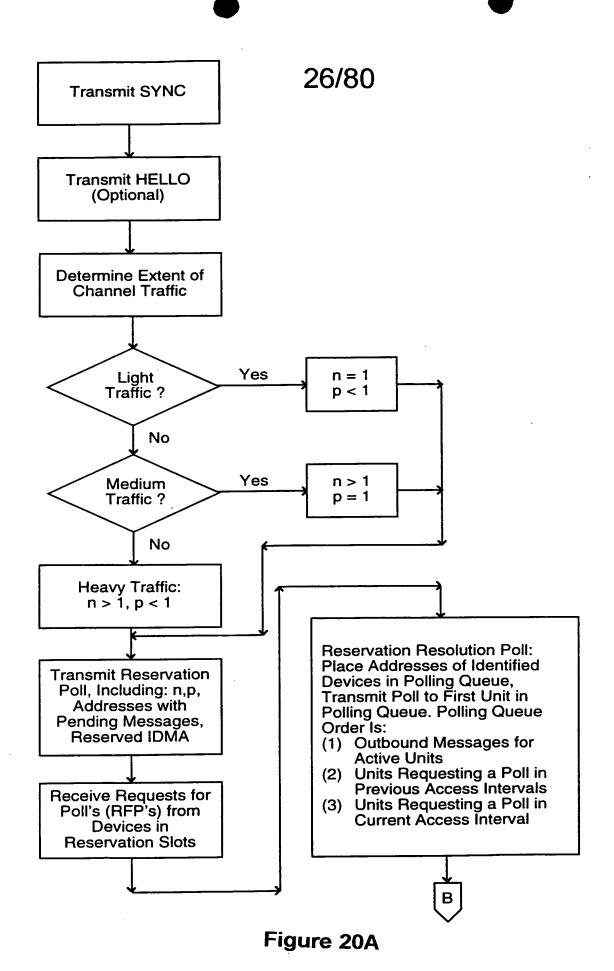


Figure 19B-2



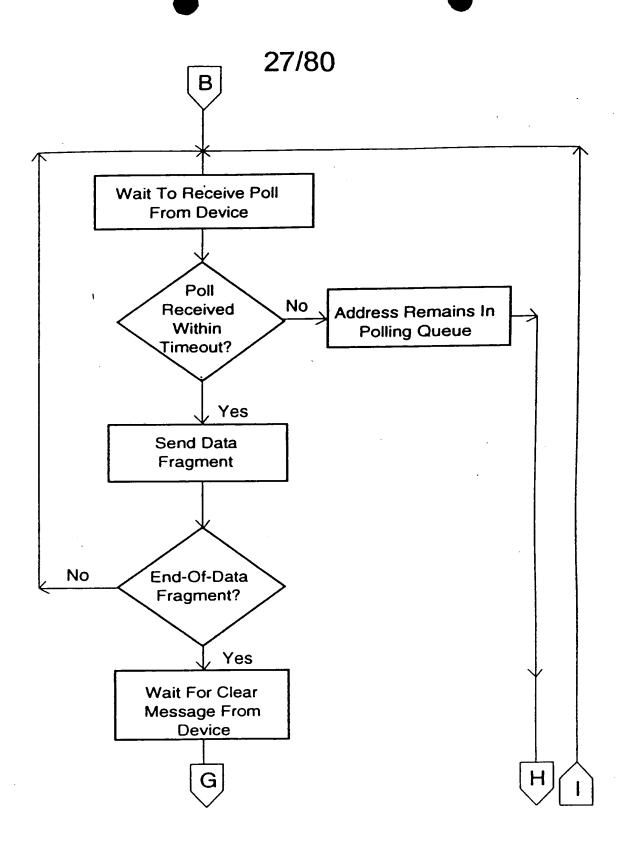


Figure 20B-1

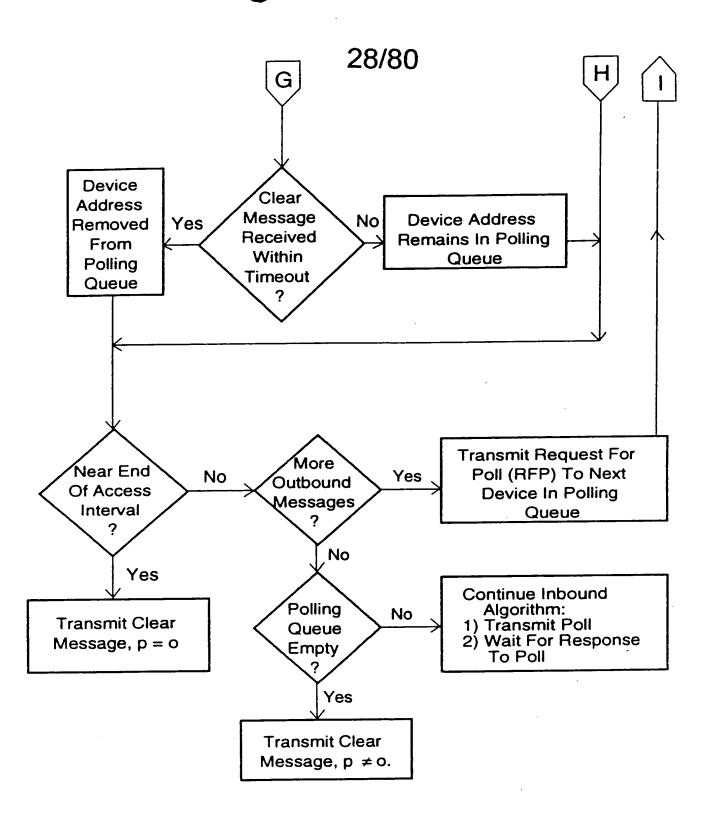
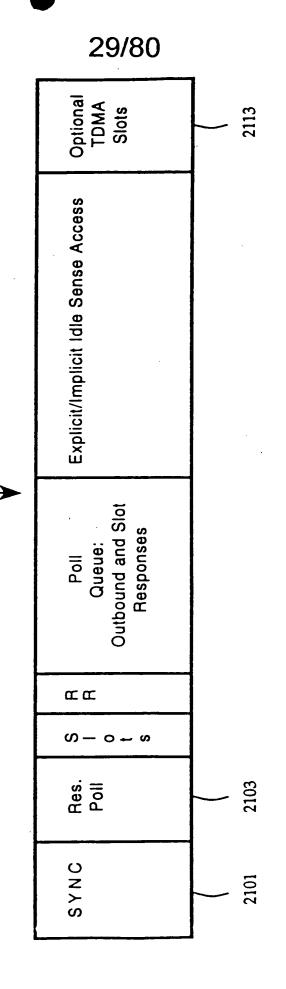


Figure 20B-2



2100

Figure 21

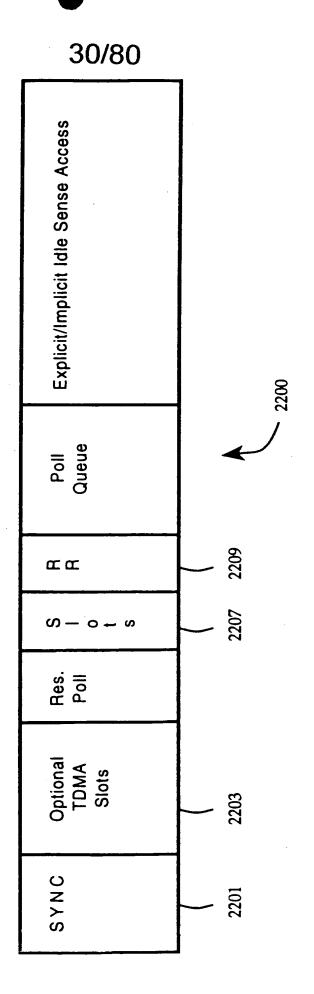


Figure 22

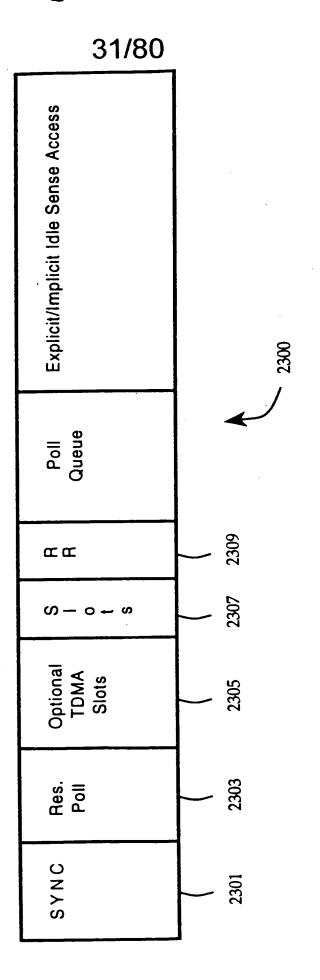
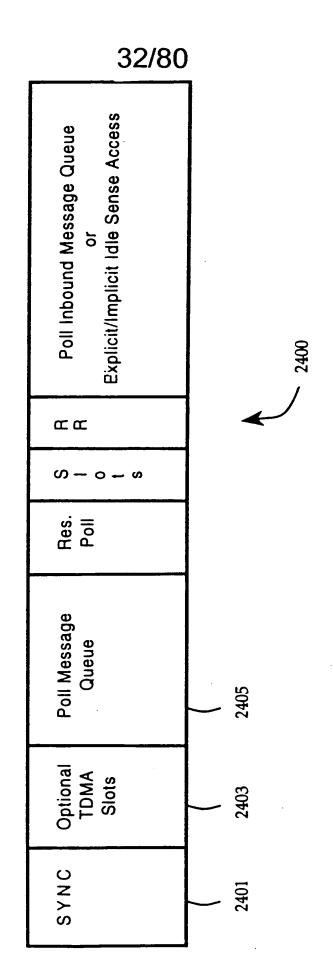


Figure 23



++ allows additional bit times for HDLC zero bit insertion

Figure 25

SYNC

1					T					4/		, 									
Definition	Frame Delimiter	Destination Address (SYNC = Broadcast = FFFF _H)	Destination Address, 2nd Byte	Message Type=SYNC, Priority Sync, or Priority SYNC with	Control Point Address	Control Point Address, 2nd Byte	System Identifier (LAN ID)	Net ID	Control Point Status	2 Bits Control Point Type	2 Bits Loading Factor	1 Bit Scheduled Service Availability	3 Bits Reserved	Scheduled Services	5 Bits Number of Allocated TDMA Time Slots	3 Bits Number of Available Time Slots	Randomly Selected Character for Improved Encryption	Error Check	Error Check	Frame Delimiter	
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	Source Address 1	Source Address 2	SYSTEM ID	NET ID	Command/Status 1					Command/Status 2			Random	CRC 1	CRC 2	FLAG	
Byte	1	2	3	4	ıc.	9	7*	*8	. 6					10*			11*	12	13	14	

Figure 26

*Optionally Encrypted Bytes

							5/6					_	_		_
Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFFF _H)	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Check	Error Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	TYPE	SLOT PARMS	,	eouenbeS	хөриј	NET PARMS				CRC 1	CRC 2	FLAG
Byte	-	2	3	4	5	9	7*	*8	* 6				10	11	12

Figure 2

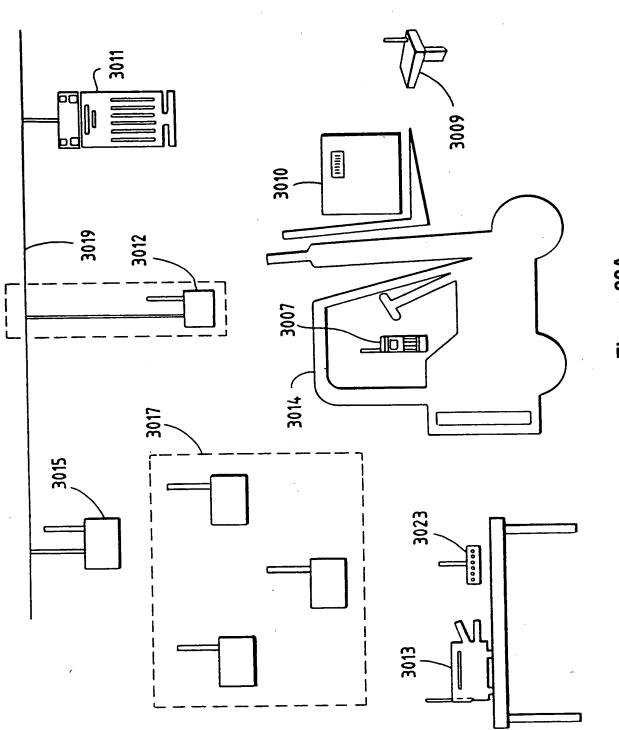
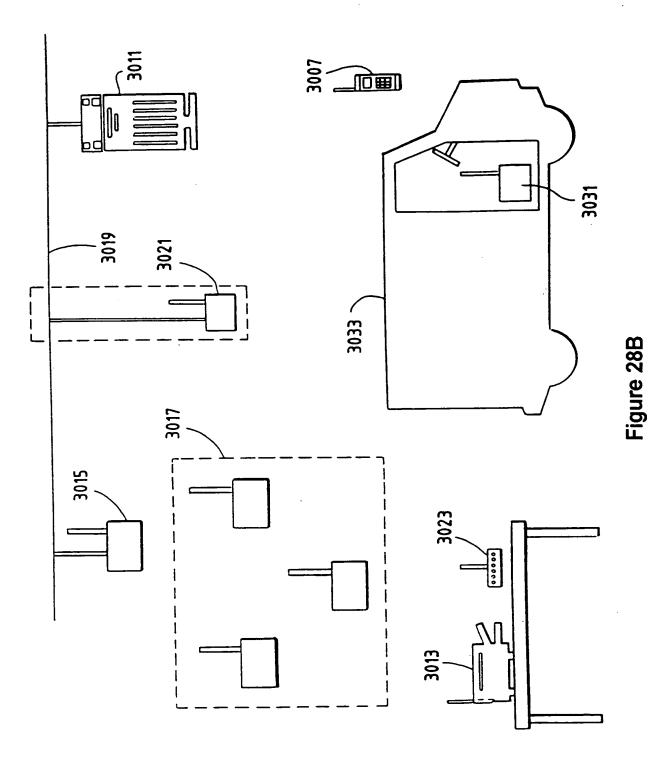
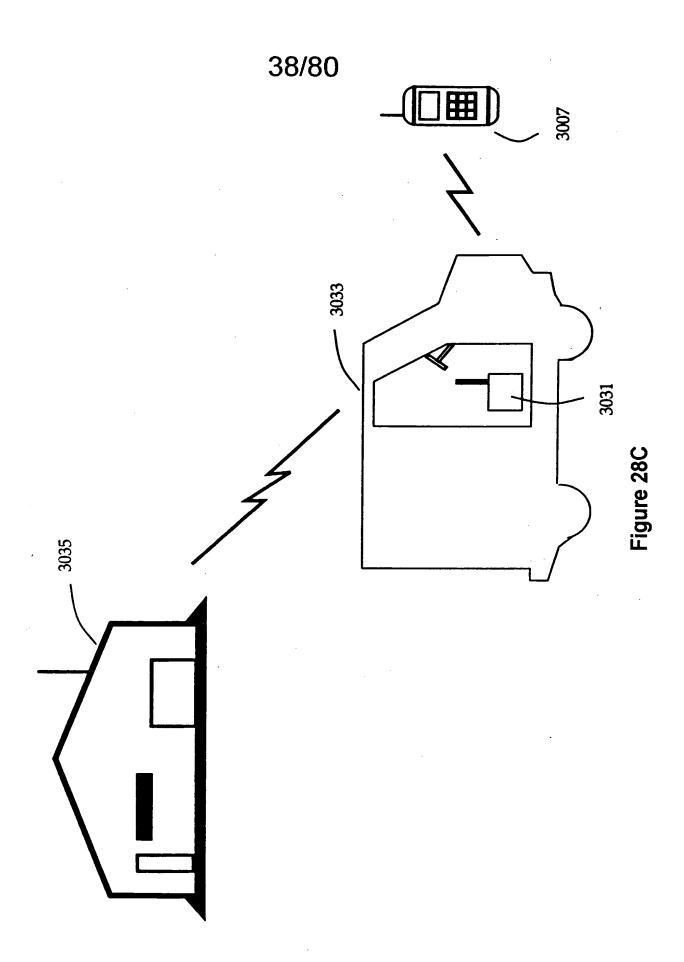
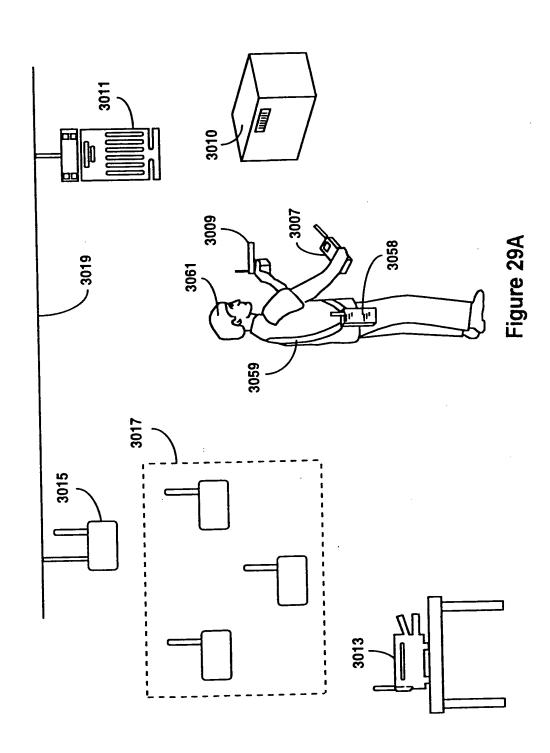
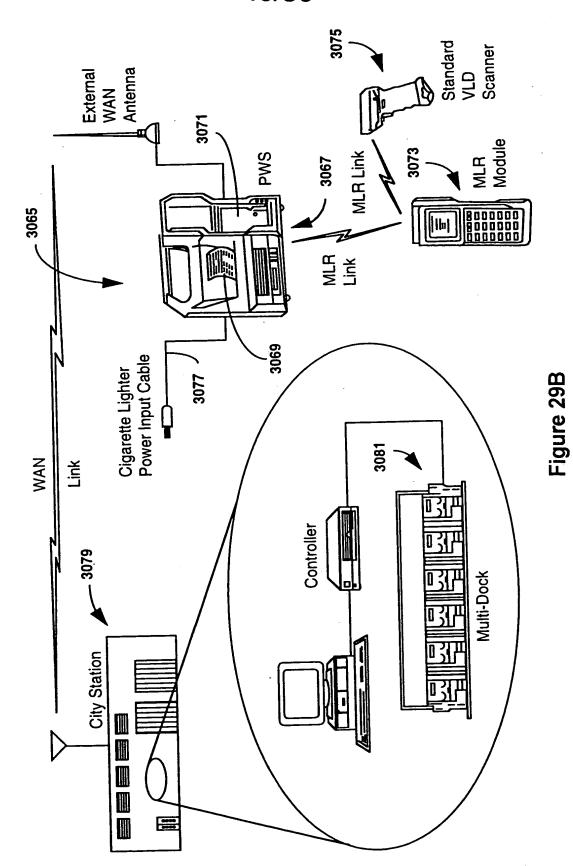


Figure 28A









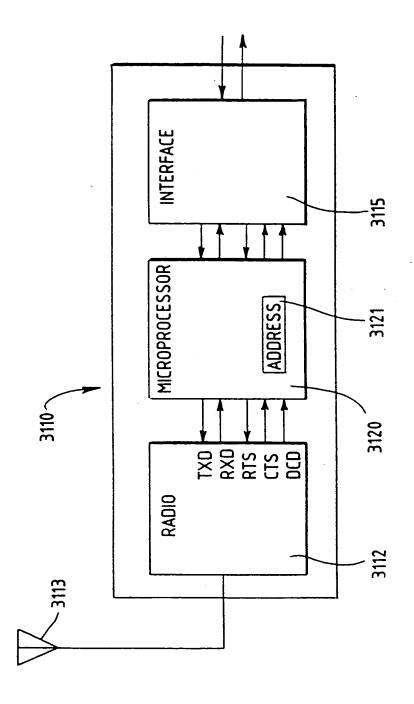
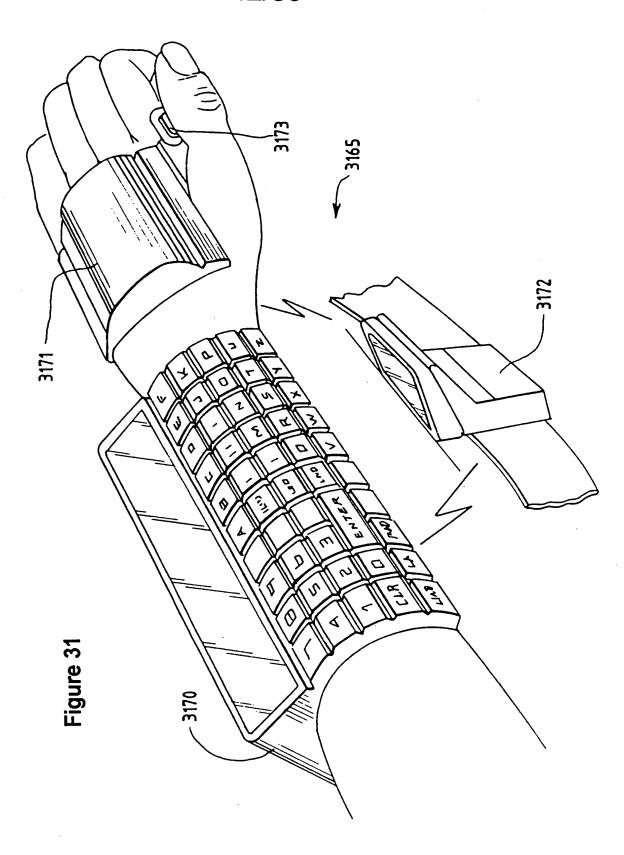
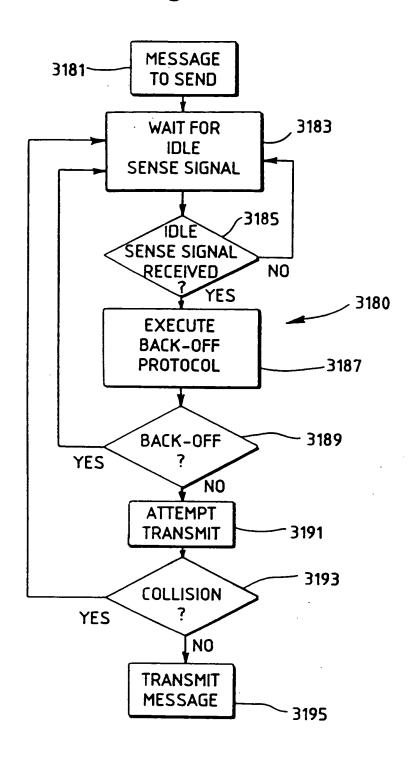


Figure 30

42/80



43/80 Figure 32



44/80

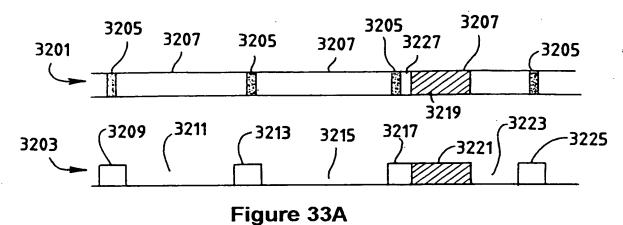


Figure 33B

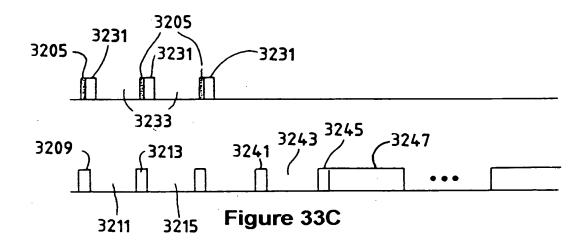


Figure 34

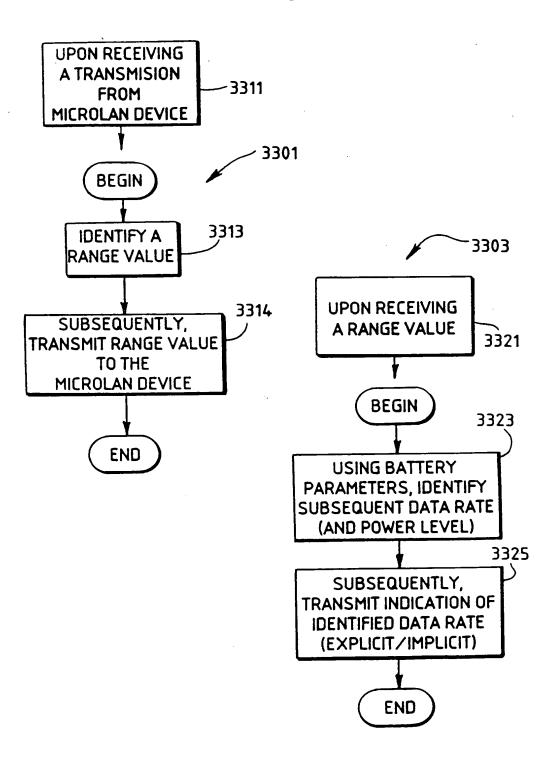
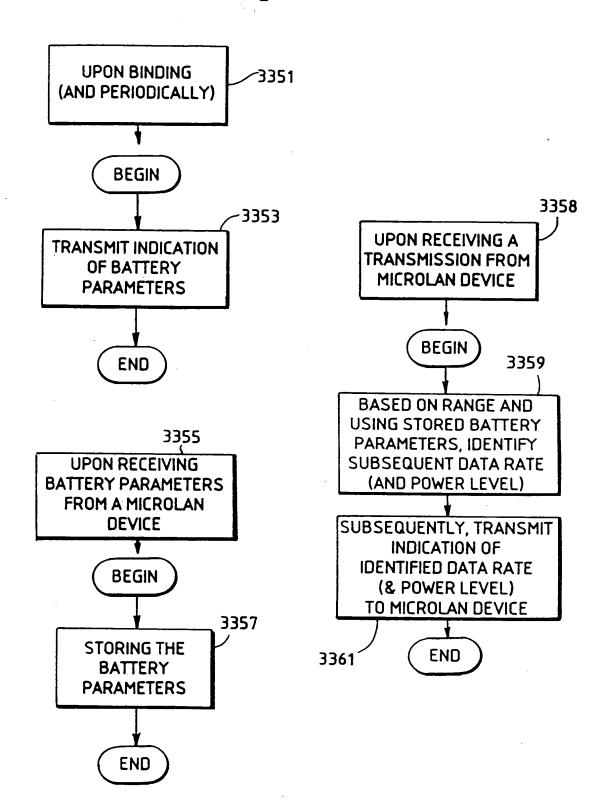


Figure 35

47/80 Figure 36



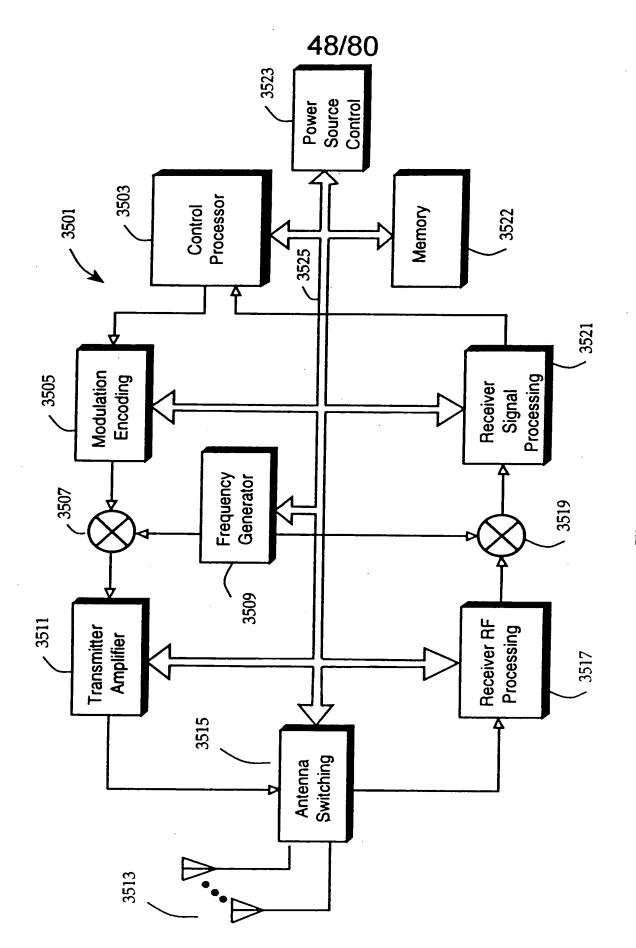


Figure 37

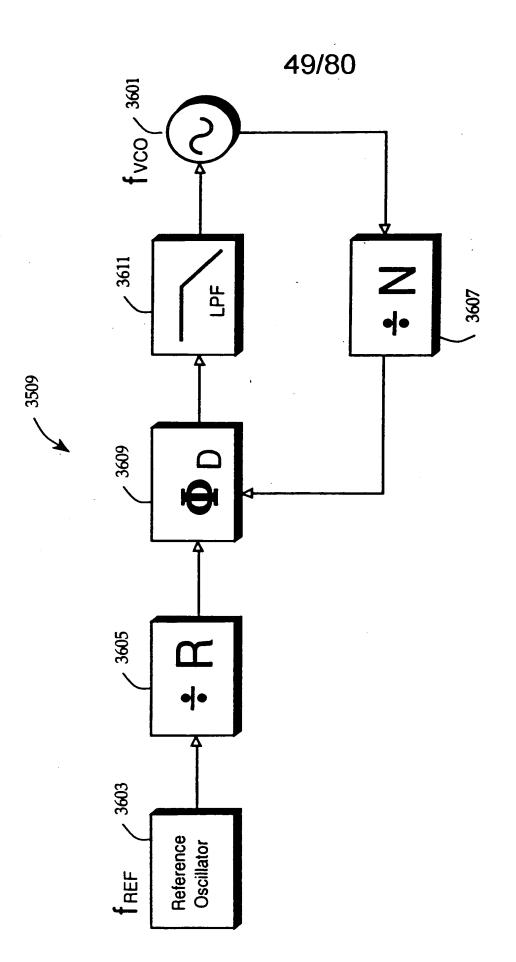


Figure 38

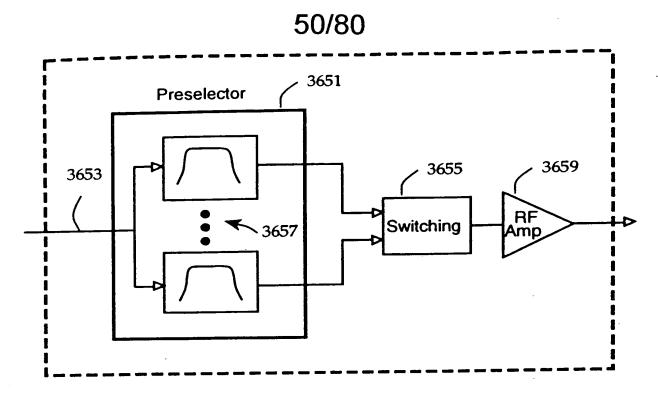


Figure 39

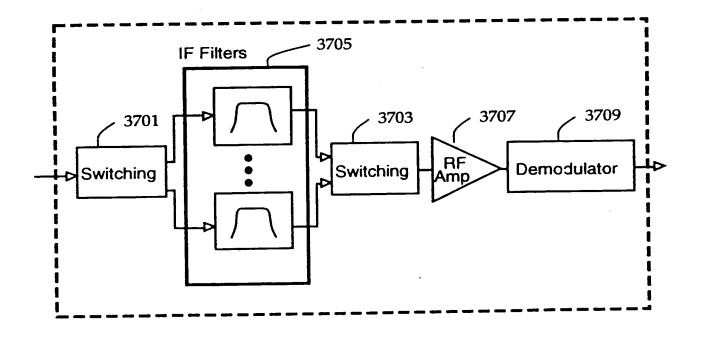


Figure 40

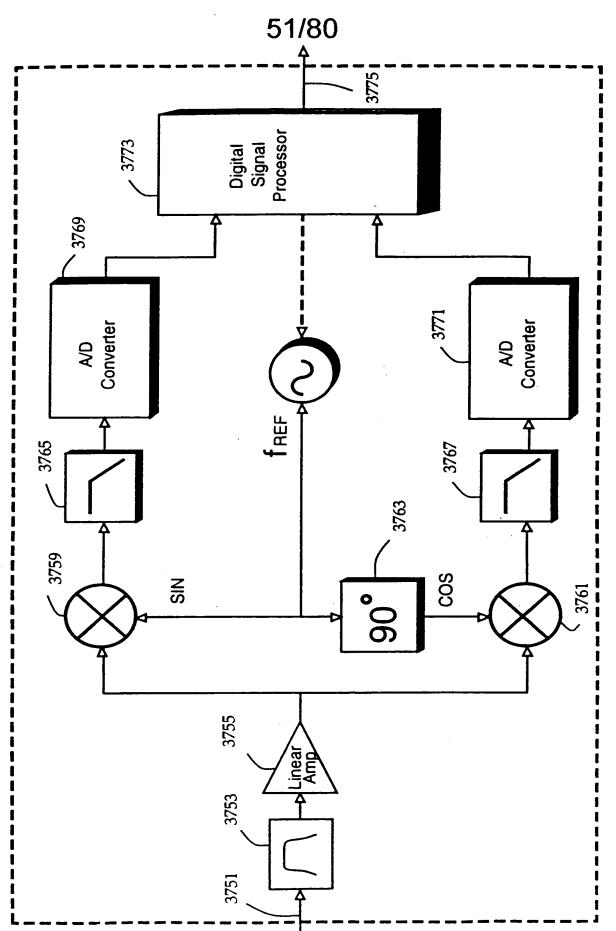
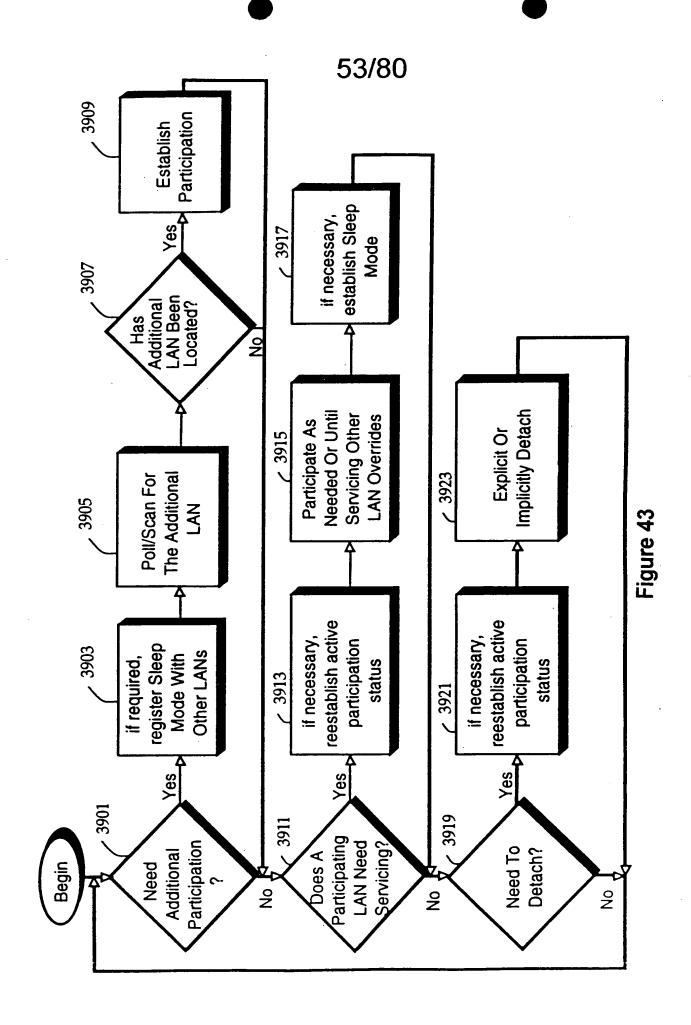


Figure 41

3823 MAC Select/Service Routines 3522 Common Mac 3819 Routines Premises LAN MAC 3811 **Routines** Peripheral LAN MAC 3813 . **Routines** Vehicular LAN 3815 **MAC Routines WAN MAC** 3817 **Routines PHY Layer** 3821 Controi

Figure 42



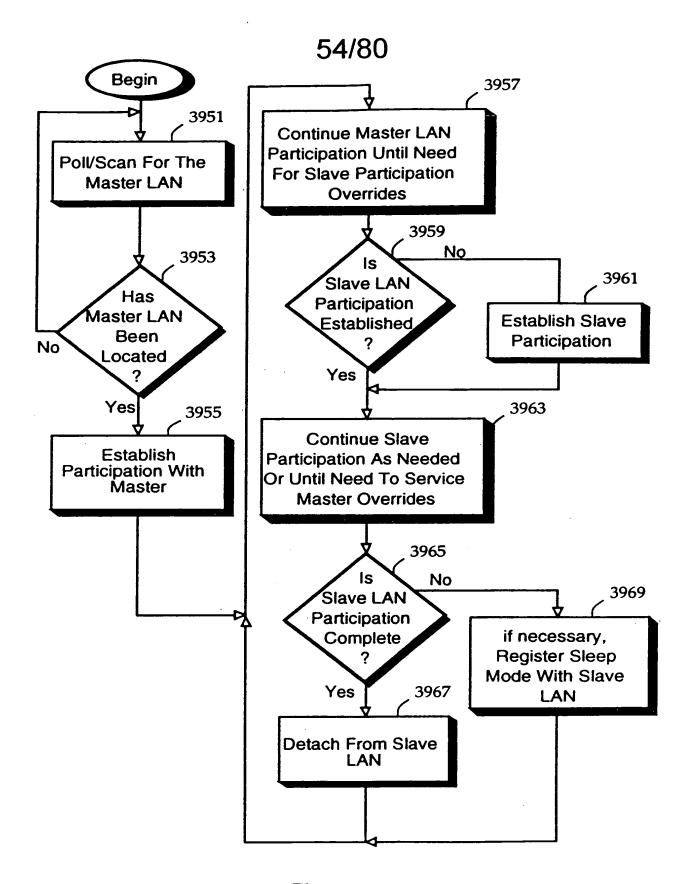


Figure 44

Figure 45



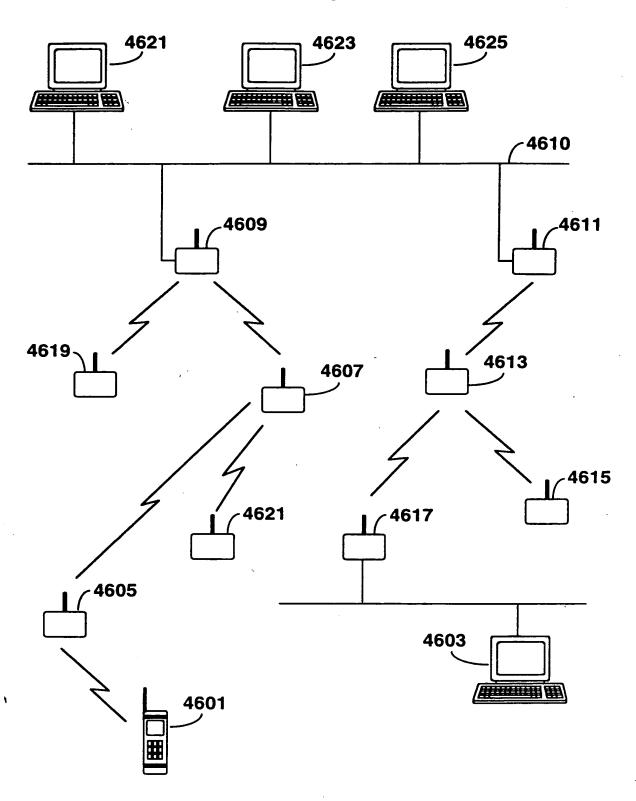


Figure 46a

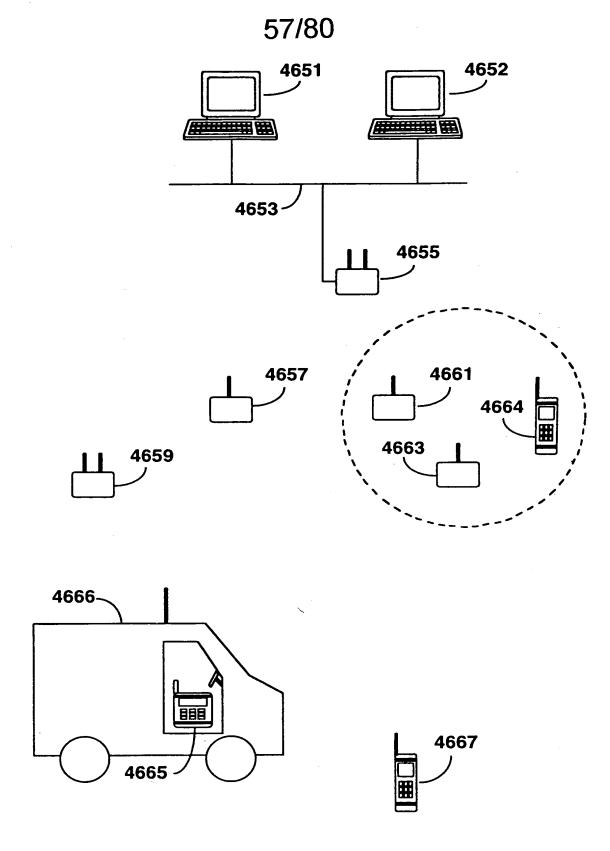


Figure 46b

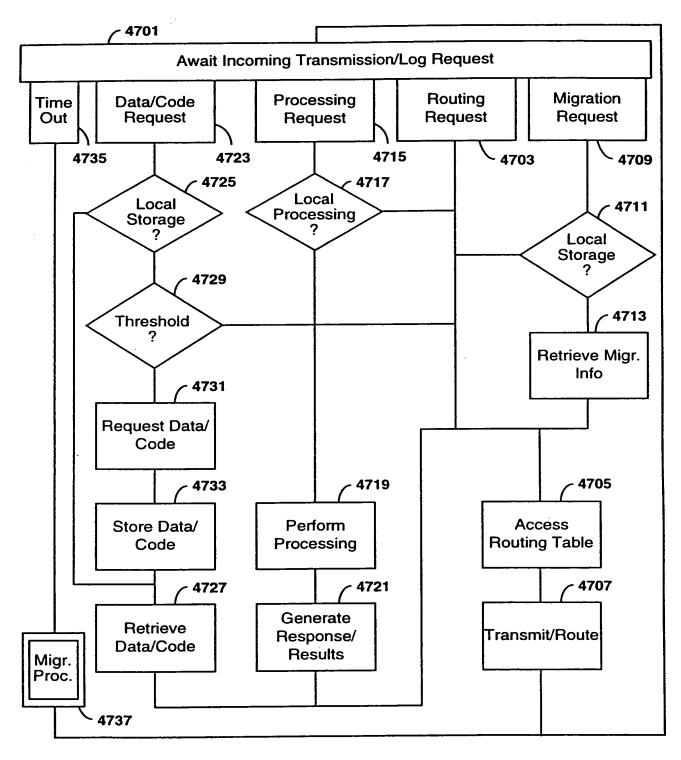
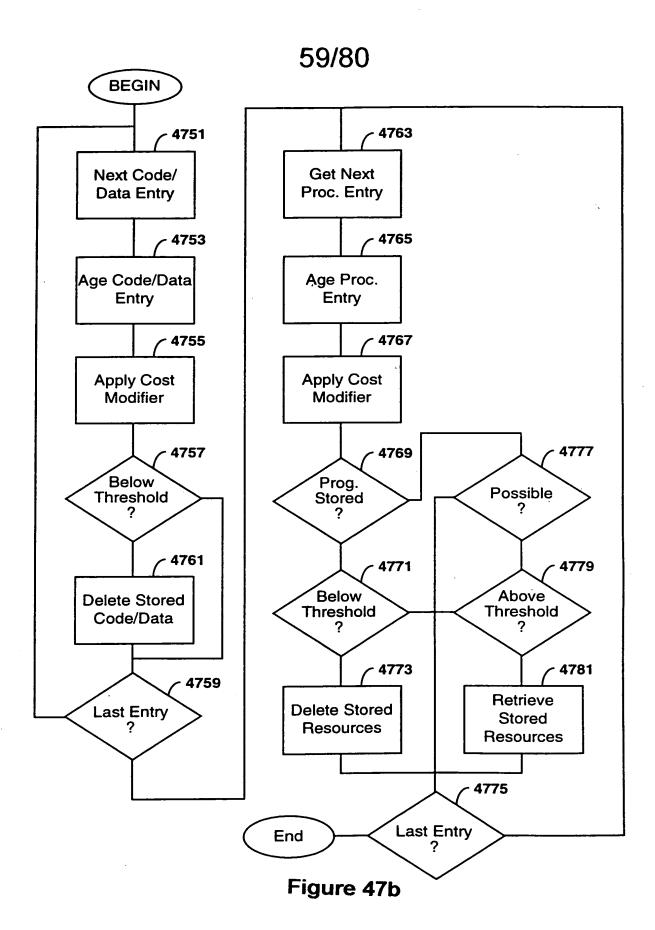


Figure 47a



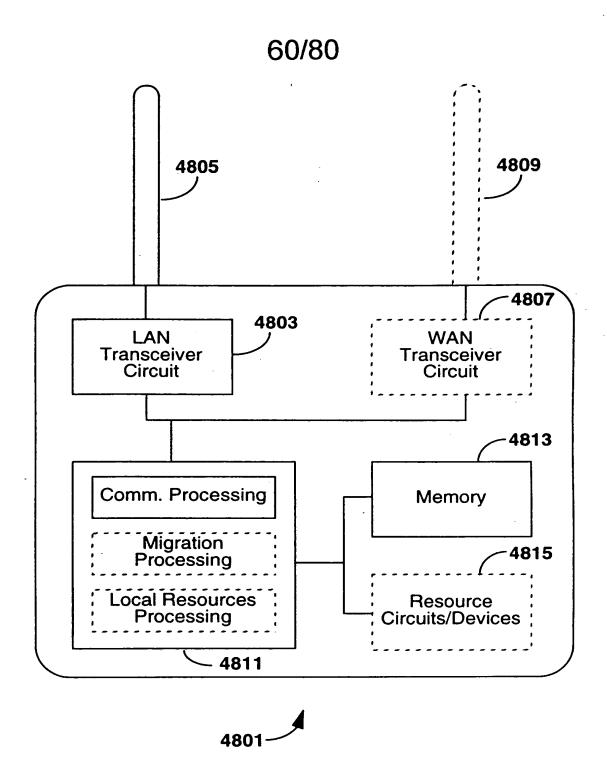


Figure 48

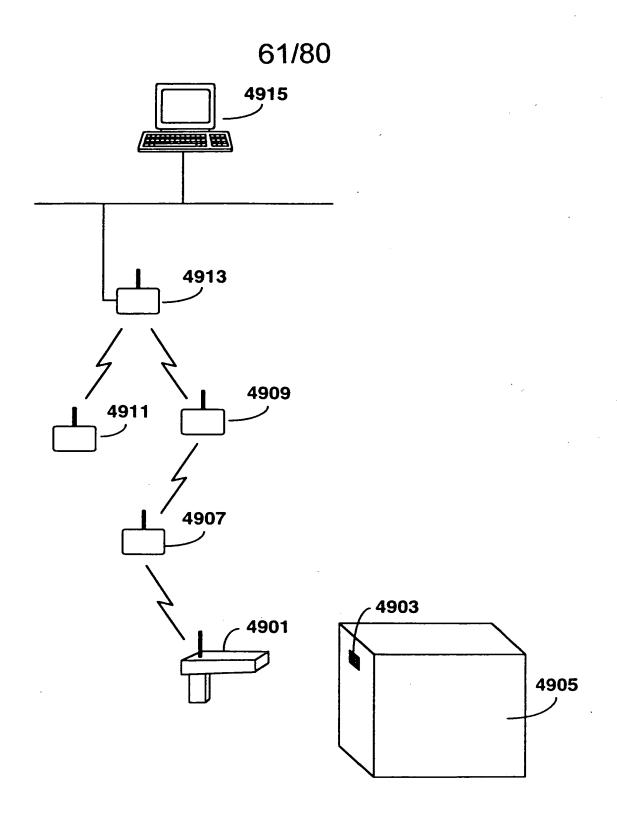


Figure 49a

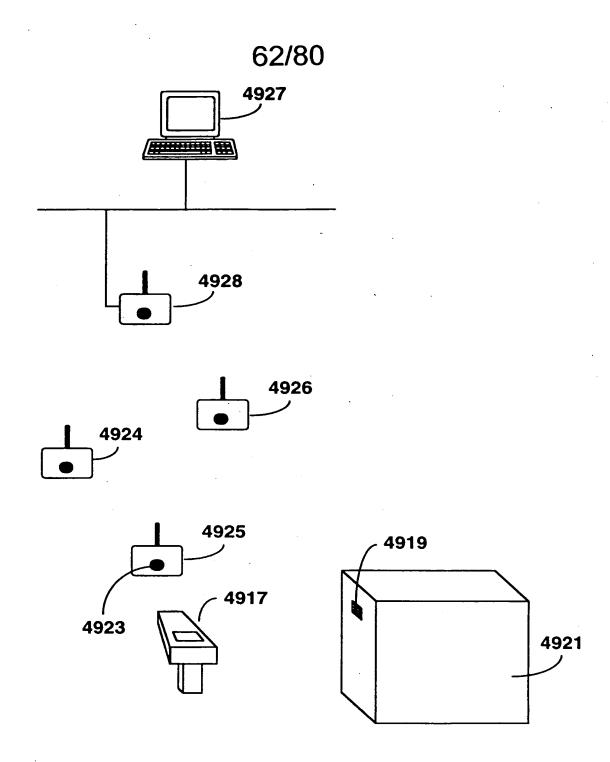


Figure 49b

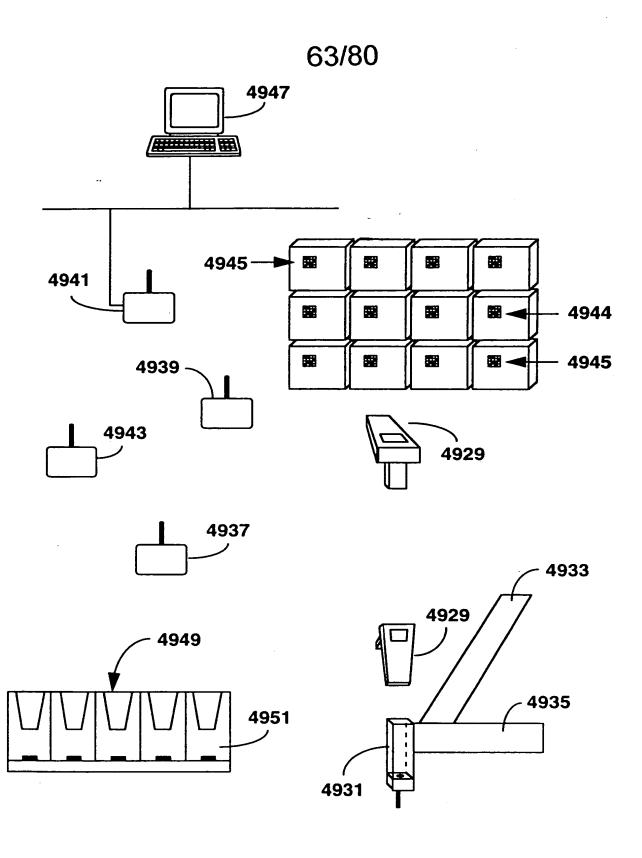


Figure 49c

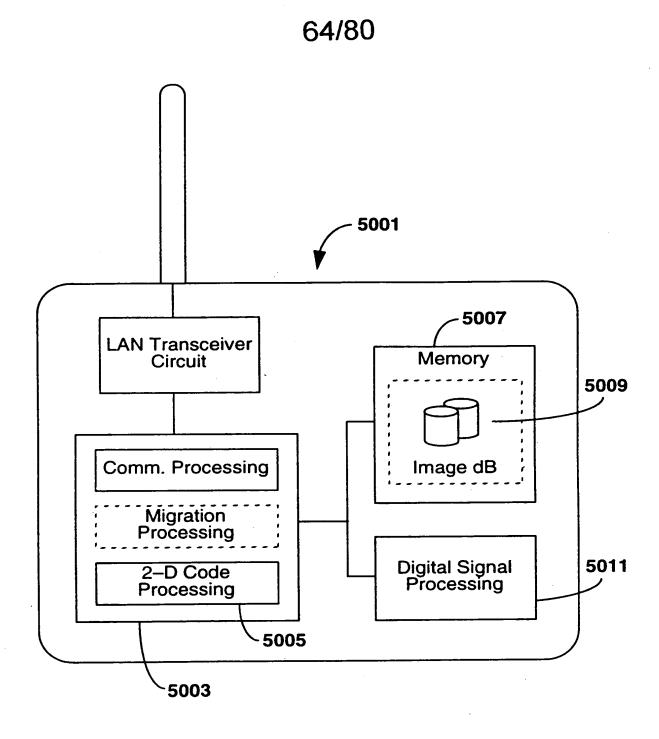


Figure 50

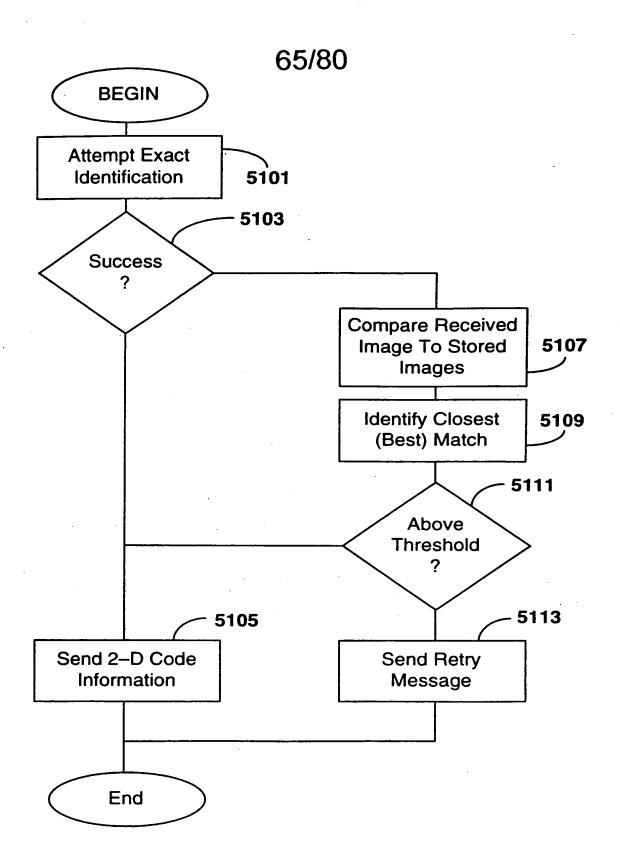


Figure 51a

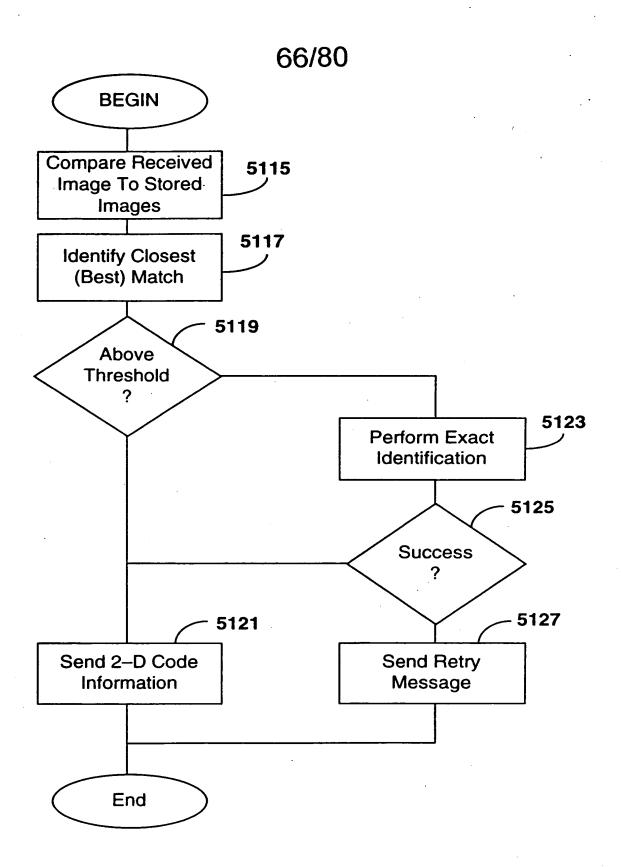
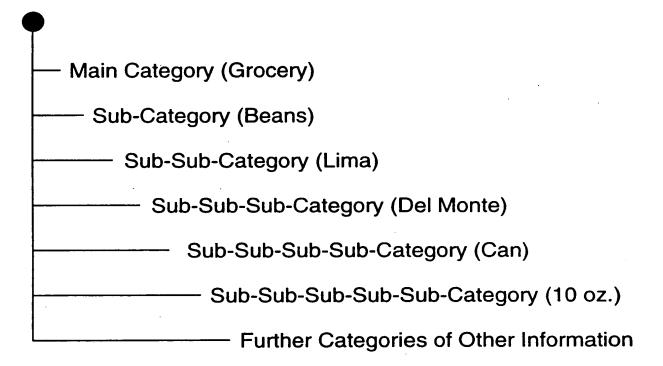


Figure 51b



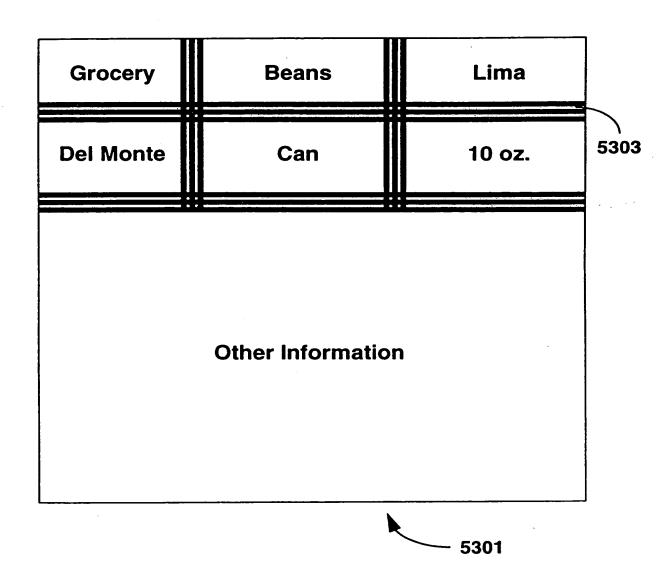


Figure 53

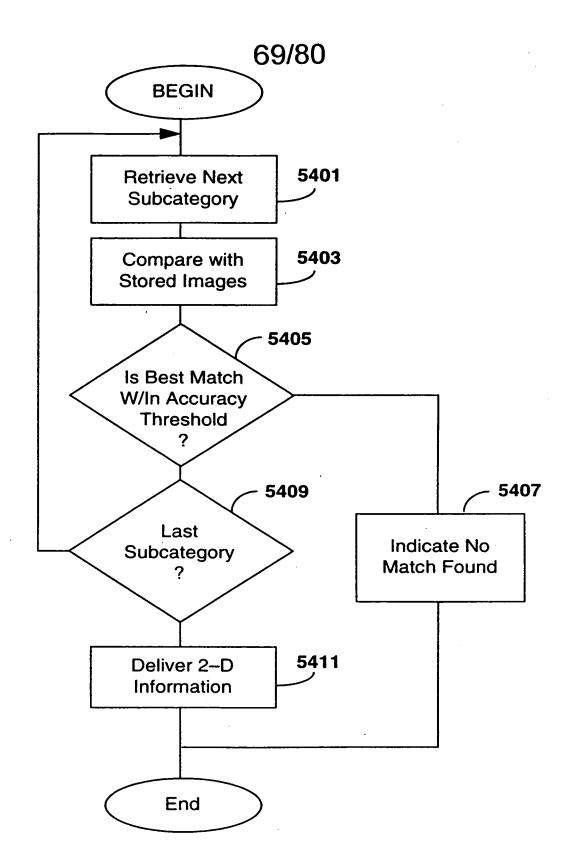
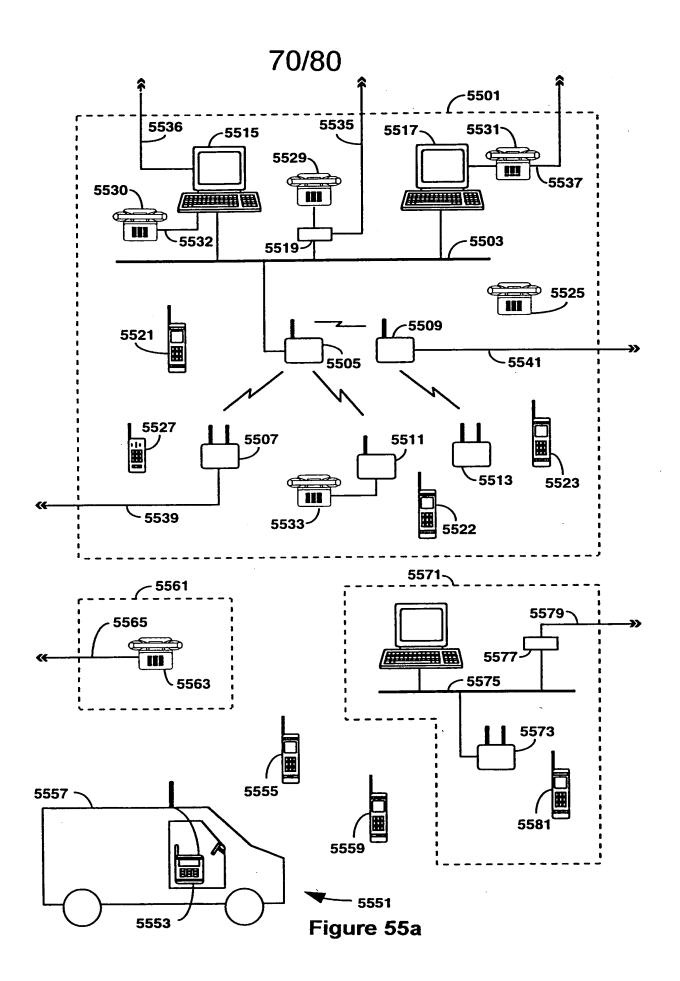


Figure 54



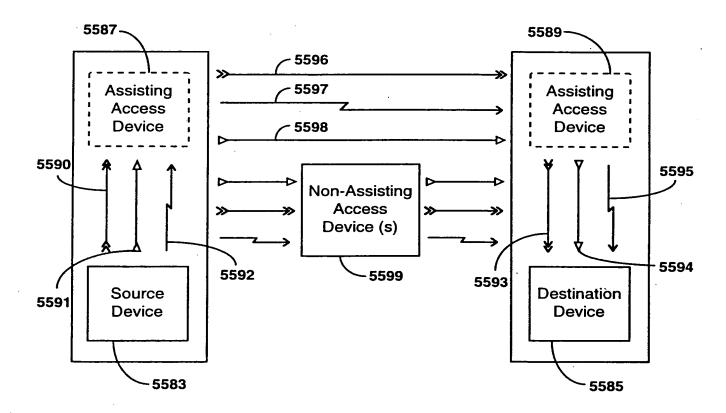
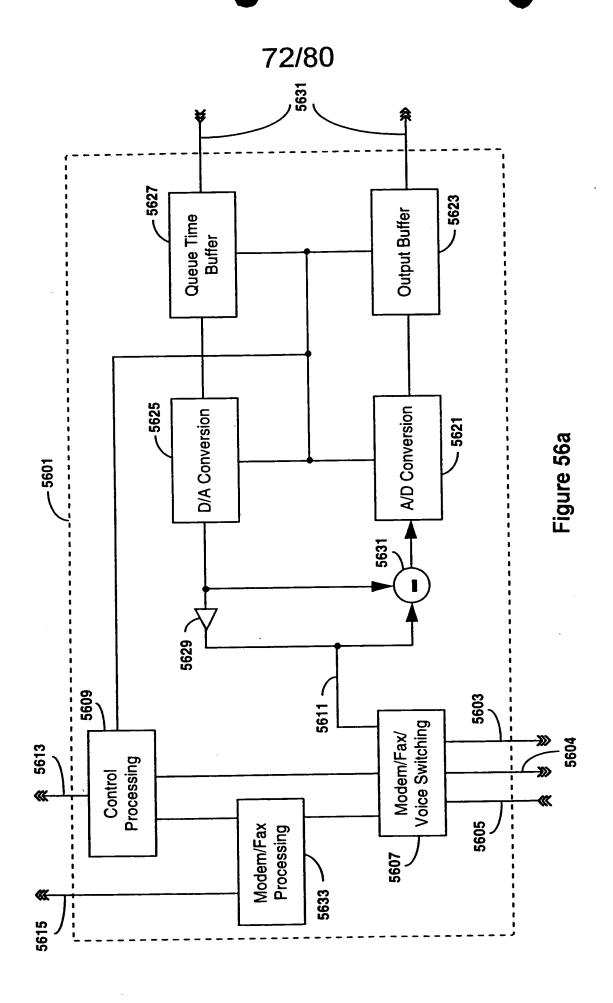
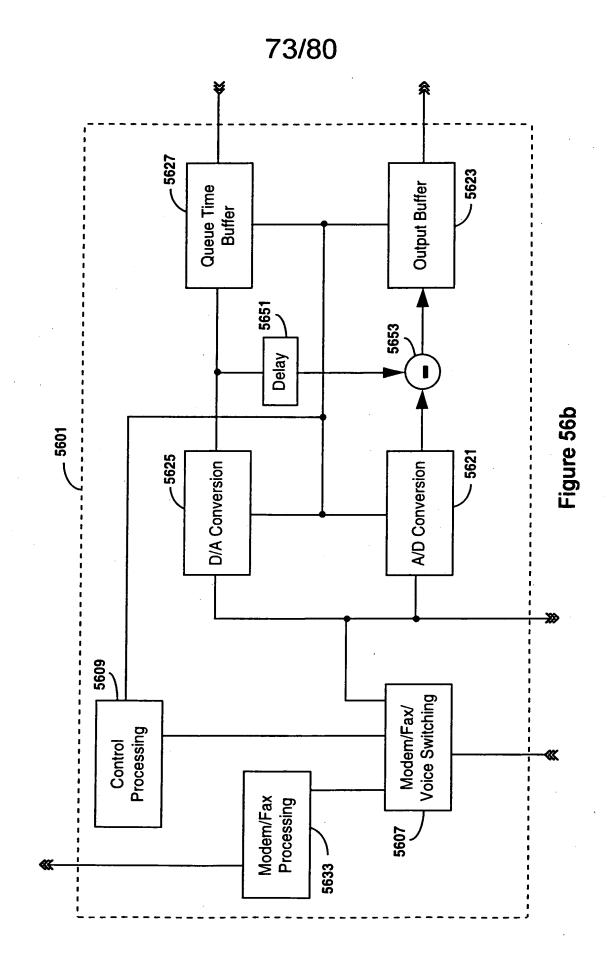


Figure 55b





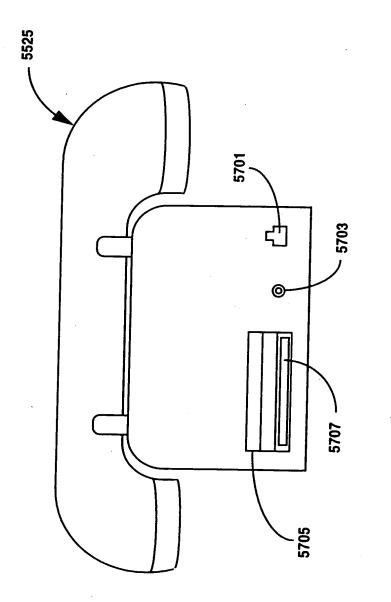


Figure 57

